

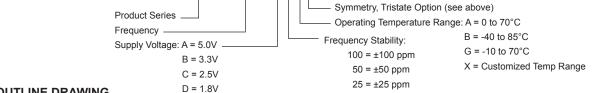
HCMOS/ACMOS/TTL COMPATIBLE SMD CLOCK OSCILLATORS - X075 Series

FEATURES

- RoHS Compliant (Pb-Free), Industry Standard Pin-out Spacing
- Very Low Phase Jitter with Fundamental or 3rd Overtone Crystal Design
- Tri-state Enable/Disable Standard; 5V, 3.3V, 2.5V or 1.8V Option
- Leadless Chip Carrier (LCC) Ultra Small Package (7x5x1.6 mm)

SPECIFICATIONS

Frequency Range	1.000 MHz to 106.25MHz (5V), to 200 MHz (3.3V)
Input Voltage (Vcc)	A = +5VDC \pm 10%; B = +3.3VDC \pm 10%; C = 2.5VDC \pm 10%; D = 1.8VDC \pm 10%
Input Current	60 mA Maximum, depending on frequency and output load
Storage Temperature	-55°C to 125°C
Overall Frequency Stability	100 = ±100 ppm; 50 = ±50 ppm; 25 = ±25 ppm
Temperature Range	A = 0°C to 70°C; B = -40°C to 85°C; D = -20°C to 70°C; G = -10°C to 70°C
Standard Stability	100A = ±100 ppm / 0°C to 70°C
Electric Option (Symmetry)	1 = Tristate 60/40%; 3 = Tristate 55/45%; 5 = Tristate 52.5/47.5% 0 = No tristate 60/40%; 2 = No tristate 55/45%; 4 = No tristate 52.5/47.5%
Output Load	HCMOS: Drive up to 50 pF load; TTL: Drive up to 10 TTL gates
Logic "1" / Logic "0" Level	0.9Vcc Minimum / 0.1Vcc Maximum
Rise/Fall Time (Tr/Tf)	10 ns Maximum, depending on frequency and output load
Start-up time	10 ms Maximum
Phase Jitter (RMS, 1 Sigma)	1 ps Maximum for fj > 1kHz; 0.3 ps Typical for fj = 12KHz to 20MHz
Tristate Function	Input (Pin 1) High (> 0.7Vcc, or 2.2V if Vcc=5V) or open: Output (Pin 3) active Input (Pin 1) Low (< 0.3Vcc, or 0.8V if Vcc=5V): Output disabled in high impedance
Output Disabled Time	100 ns Maximum
Output Enable Time	100 ns Maximum
Creating a Part Number	X075-125M000-B50A3



OUTLINE DRAWING

